

# KA5x0165Rxx-SERIES

KA5H0165R/RN, KA5M0165R/RN/RI, KA5L0165R/RN/RI/KA5H0165RV/RVN, KA5M0165RV/RVN, KA5L0165RV/RVN

## Fairchild Power Switch(FPS)

### Features

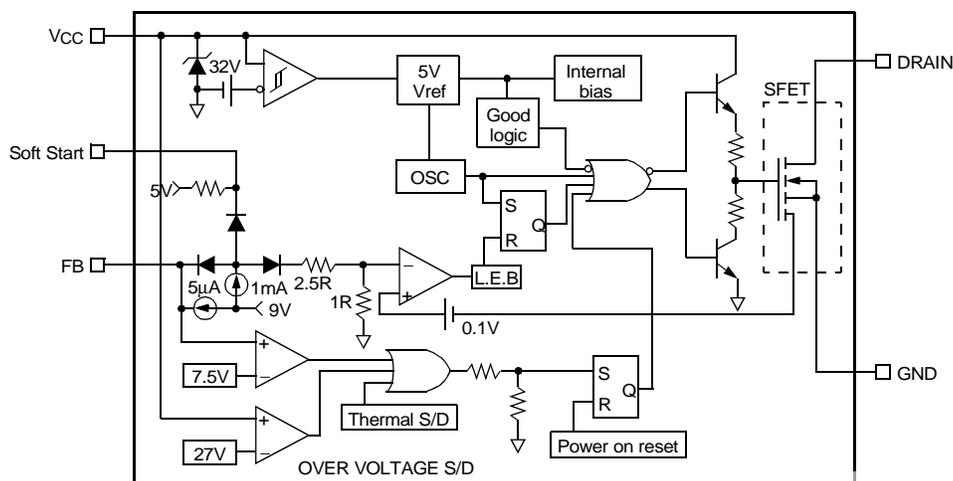
- Precision fixed operating frequency (100 / 67 / 50KHz )
- Pulse by pulse current limiting
- Over current protection
- Over voltage protection (Min. 25V) -KA5x0165R/RN/RI
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- Auto-restart mode

### Description

The Fairchild Power Switch(FPS) product family is specially designed for an off-line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM IC. Included PWM controller features integrated fixed oscillator, under voltage lock out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shut down protection, over voltage protection, and temperature compensated precision current sources for loop compensation and fault protection circuitry. compared to discrete MOSFET and controller or RCC switching converter solution, aFairchild Power Switch(FPS) can reduce total component count, design size, weight and at the same time increase efficiency, productivity, and system reliability. It has a basic platform well suited for cost-effective design in either a flyback converter or a forward converter.

TO-220F-4L	8-DIP	I2-PAK-5L
		
1. GND 2. Drain 3. Vcc 4. FB	1.6.7.8. Drain 2. GND 3. Vcc 4. FB 5. NC	1. GND 2. Drain 3. Vcc 4. FB 5. S/S

### Internal Block Diagram



## Absolute Maximum Ratings

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
Maximum Drain Voltage	V <sub>D,MAX</sub>	650	V
Drain-Gate Voltage (R <sub>GS</sub> =1MΩ)	V <sub>DGR</sub>	650	V
Gate-Source (GND) Voltage	V <sub>GS</sub>	±30	V
Drain Current Pulsed <sup>(1)</sup>	I <sub>DM</sub>	4.0	ADC
Continuous Drain Current (T <sub>C</sub> =25°C)	I <sub>D</sub>	1.0	ADC
Continuous Drain Current (T <sub>C</sub> =100°C)	I <sub>D</sub>	0.7	ADC
Single Pulsed Avalanche Energy <sup>(2)</sup>	E <sub>AS</sub>	95	mJ
Maximum Supply Voltage	V <sub>CC,MAX</sub>	30	V
Analog Input Voltage Range	V <sub>FB</sub>	-0.3 to V <sub>SD</sub>	V
Total Power Dissipation	P <sub>D</sub>	40	W
	Derating	0.32	W/°C
Operating Junction Temperature.	T <sub>J</sub>	+160	°C
Operating Ambient Temperature.	T <sub>A</sub>	-25 to +85	°C
Storage Temperature Range.	T <sub>STG</sub>	-55 to +150	°C

### Note:

1. Repetitive rating: Pulse width limited by maximum junction temperature
2. L=24mH, starting T<sub>J</sub>=25°C

## Electrical Characteristics (SFET part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
Zero gate voltage drain current	IDSS	VDS=Max. Rating, VGS=0V	-	-	50	μA
		VDS=0.8Max. Rating, VGS=0V, TC=125°C	-	-	200	μA
Static drain-source on resistance <sup>(note)</sup>	RDS(ON)	VGS=10V, ID=0.5A	-	8	10	Ω
Forward transconductance <sup>(note)</sup>	gfs	VDS=50V, ID=0.5A	0.5	-	-	S
Input capacitance	Ciss	VGS=0V, VDS=25V, f=1MHz	-	250	-	pF
Output capacitance	Coss		-	25	-	
Reverse transfer capacitance	Crss		-	10	-	
Turn on delay time	td(on)	VDD=0.5BVDSS, ID=1.0A (MOSFET switching time are essentially independent of operating temperature)	-	12	-	nS
Rise time	tr		-	4	-	
Turn off delay time	td(off)		-	30	-	
Fall time	tf		-	10	-	
Total gate charge (gate-source+gate-drain)	Qg	VGS=10V, ID=1.0A, VDS=0.5BVDSS (MOSFET switching time are essentially independent of operating temperature)	-	-	21	nC
Gate-source charge	Qgs		-	3	-	
Gate-drain (Miller) charge	Qgd		-	9	-	

### Note:

Pulse test: Pulse width ≤ 300μS, duty cycle ≤ 2%

$$S = \frac{1}{R}$$

## Electrical Characteristics (CONTROL part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>UVLO SECTION</b>						
Start Threshold Voltage	VSTART	VFB=GND	14	15	16	V
Stop Threshold Voltage	VSTOP	VFB=GND	8.4	9	9.6	V
<b>OSCILLATOR SECTION</b>						
Initial accuracy	FOSC	KA5H0165Rxx	90	100	110	kHz
Initial accuracy	FOSC	KA5M0165Rxx	61	67	73	kHz
Initial accuracy	FOSC	KA5L0165Rxx	45	50	55	kHz
Frequency change with temperature <sup>(2)</sup>	$\Delta F/\Delta T$	-25°C≤Ta≤+85°C	-	±5	±10	%
Maximum duty cycle	Dmax	KA5H0165Rxx	62	67	72	%
Maximum duty cycle	Dmax	KA5M0165Rxx KA5L0165Rxx	72	77	82	%
<b>FEEDBACK SECTION</b>						
Feedback source current	IFB	Ta=25°C, 0V≤Vfb≤3V	0.7	0.9	1.1	mA
Shutdown feedback voltage	VSD	Vfb≥6.5V	6.9	7.5	8.1	V
Shutdown delay current	I <sub>delay</sub>	Ta=25°C, 3V≤Vfb≤VSD	4	5	6	μA
<b>SOFT START SECTION</b>						
Soft Start Voltage	VSS	KA5x0165RI	4.7	5.0	5.3	V
Soft Start Current	I <sub>SS</sub>		0.8	1.0	1.2	mA
<b>REFERENCE SECTION</b>						
Output voltage <sup>(1)</sup>	V <sub>ref</sub>	Ta=25°C	4.80	5.00	5.20	V
Temperature Stability <sup>(1)(2)</sup>	V <sub>ref</sub> /ΔT	-25°C≤Ta≤+85°C	-	0.3	0.6	mV/°C
<b>CURRENT LIMIT(SELF-PROTECTION)SECTION</b>						
Peak Current Limit	I <sub>OVER</sub>	Max. inductor current	0.53	0.6	0.67	A
<b>PROTECTION SECTION</b>						
Thermal shutdown temperature (Tj) <sup>(1)</sup>	TSD	-	140	160	-	°C
Over voltage protection	V <sub>OVP</sub>	KA5x0165R/RN/RI	25	27	29	V
<b>TOTAL STANDBY CURRENT SECTION</b>						
Start Up Current	I <sub>START</sub>	V <sub>CC</sub> =14V	-	100	170	μA
Operating supply current (control part only)	I <sub>OP</sub>	V <sub>CC</sub> ≤28	-	7	12	mA

**Note:**

1. These parameters, although guaranteed, are not 100% tested in production
2. These parameters, although guaranteed, are tested in EDS (wafer test) process

## Typical Performance Characteristics

(These characteristic graphs are normalized at  $T_a=25^\circ\text{C}$ )

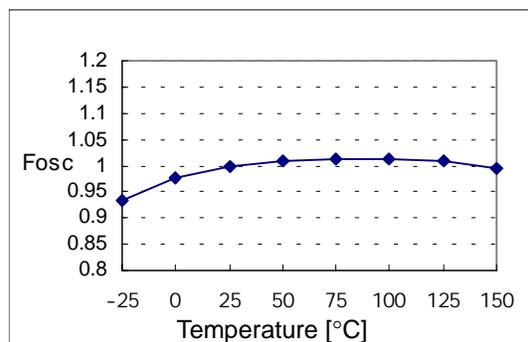


Figure 1. Operating Frequency

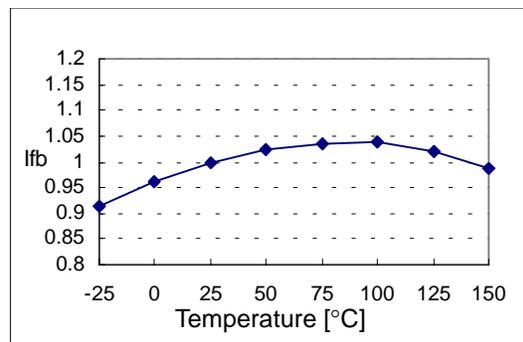


Figure 2. Feedback Source Current

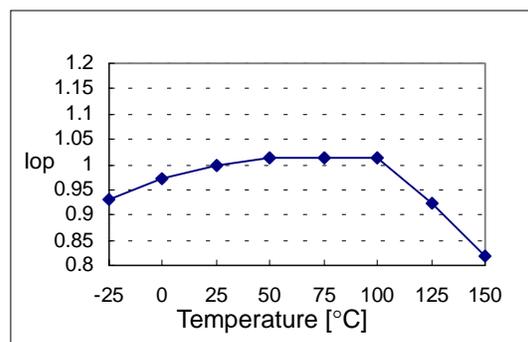


Figure 3. Operating Supply Current

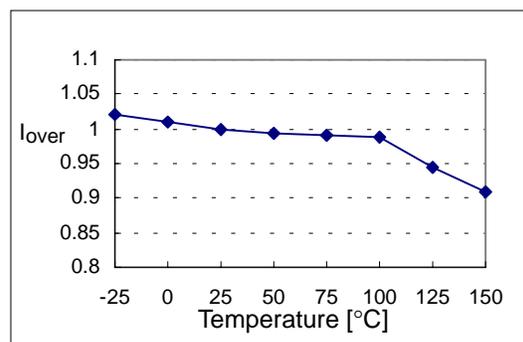


Figure 4. Peak Current Limit

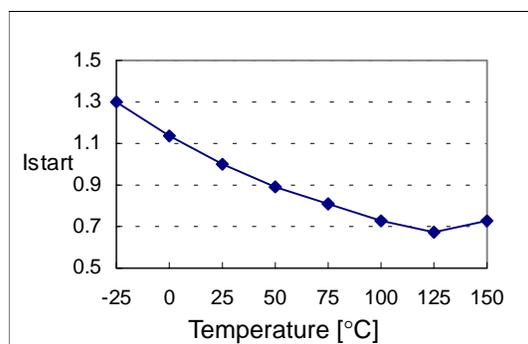


Figure 5. Start up Current

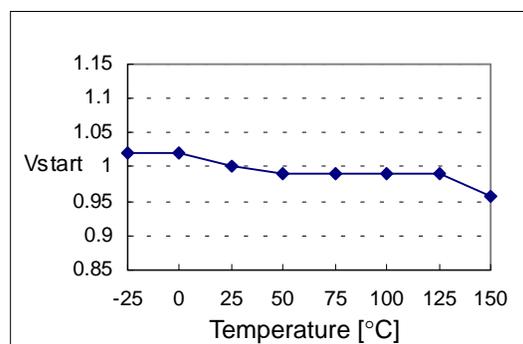


Figure 6. Start Threshold Voltage

## Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta=25°C)

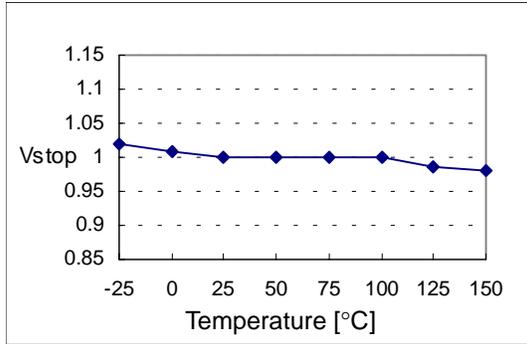


Figure 7. Stop Threshold Voltage

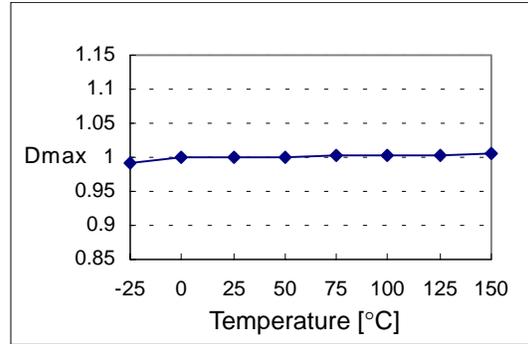


Figure 8. Maximum Duty Cycle

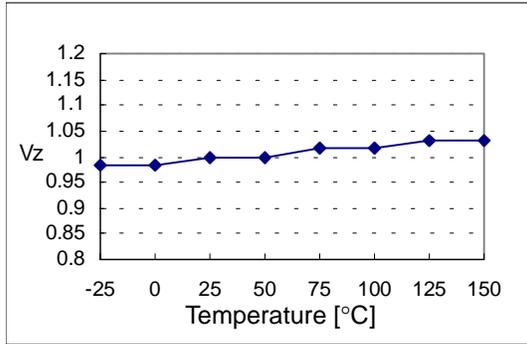


Figure 9. VCC Zener Voltage

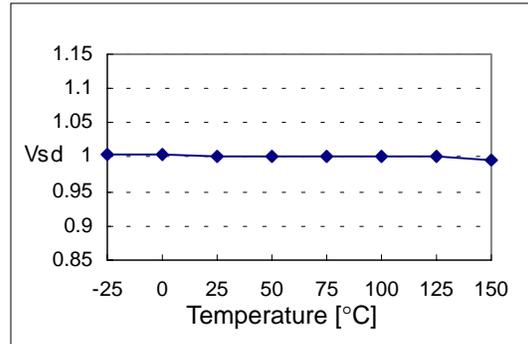


Figure 10. Shutdown Feedback Voltage

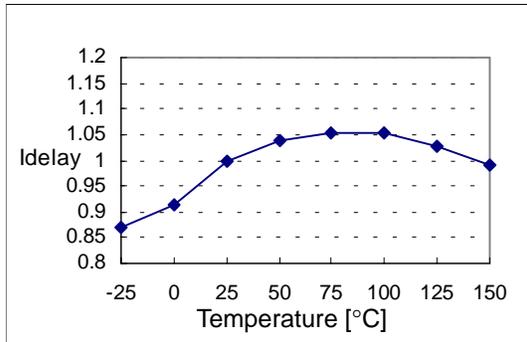


Figure 11. Shutdown Delay Current

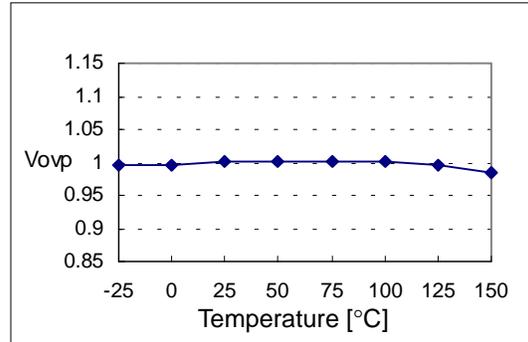


Figure 12. Over Voltage Protection

## Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at  $T_a=25^\circ\text{C}$ )

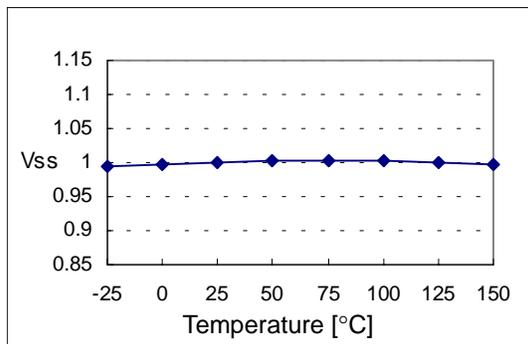


Figure13. Soft Start Voltage

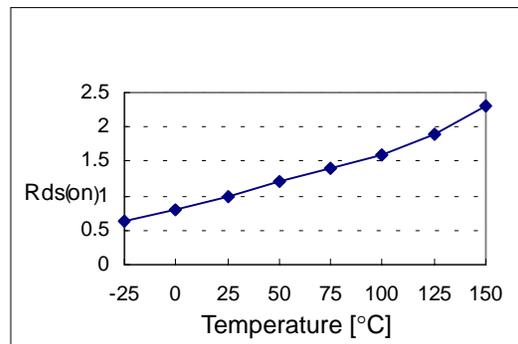
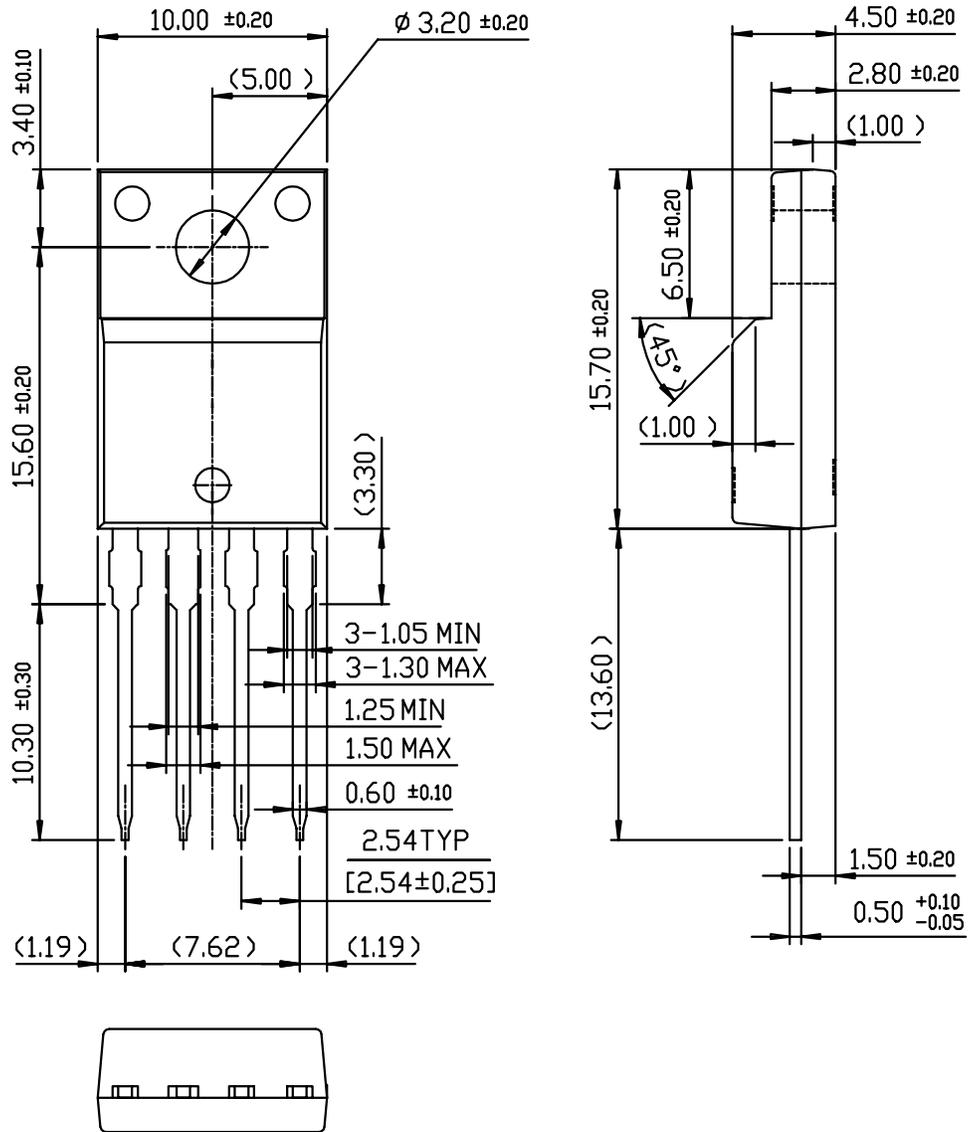


Figure 14. Static Drain-Source on Resistance

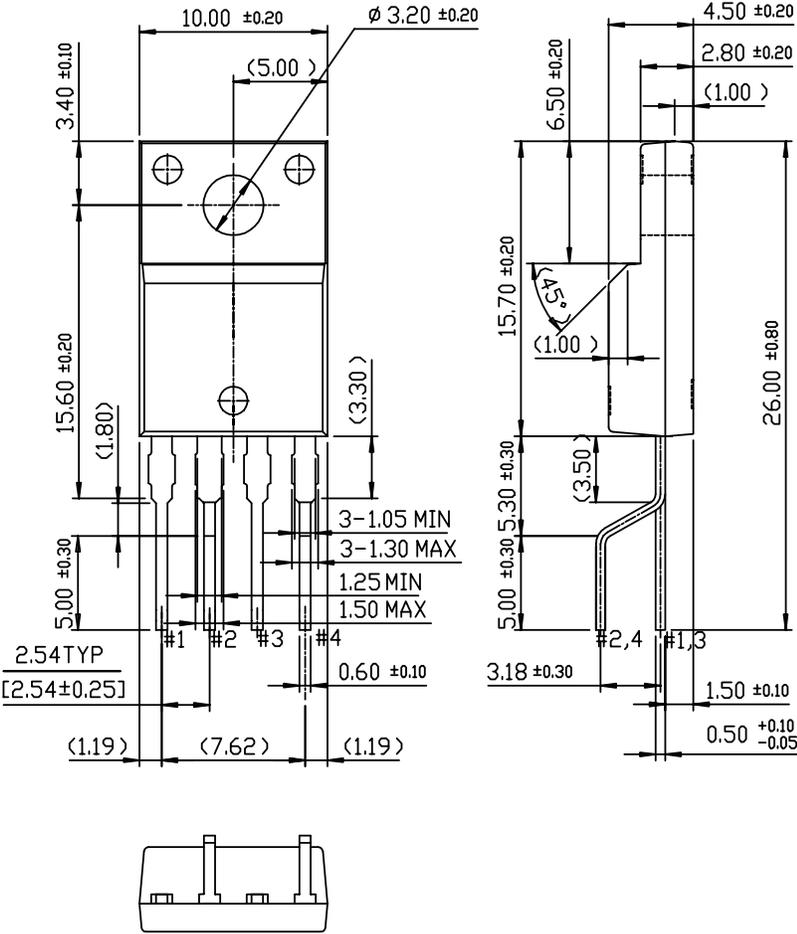
# Package Dimensions

## TO-220F-4L



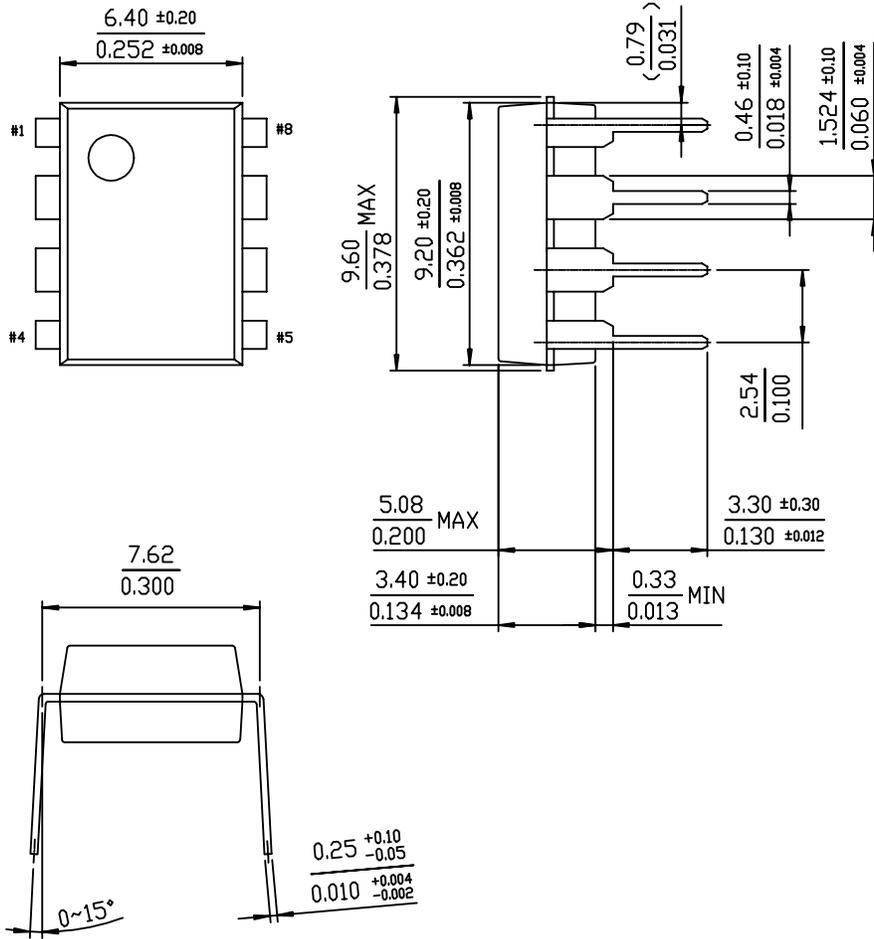
Package Dimensions (Continued)

### TO-220F-4L(Forming)



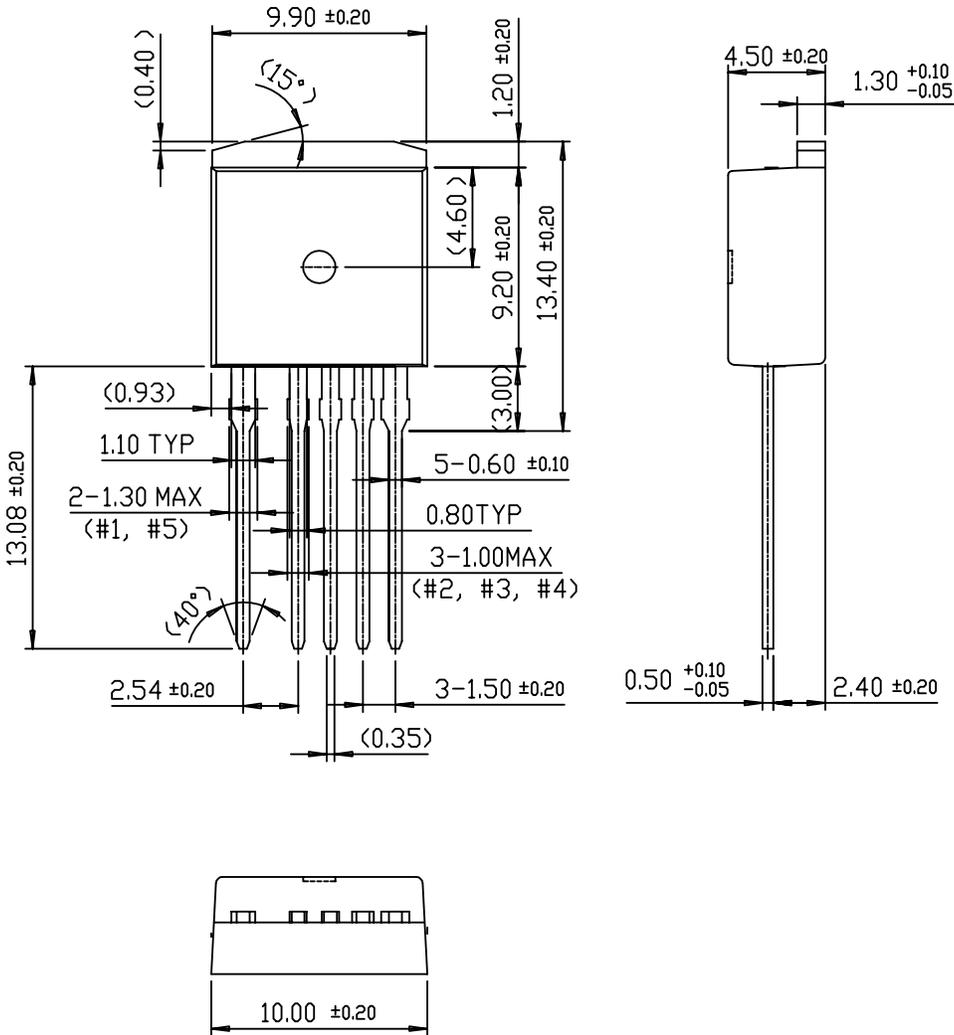
Package Dimensions (Continued)

8-DIP



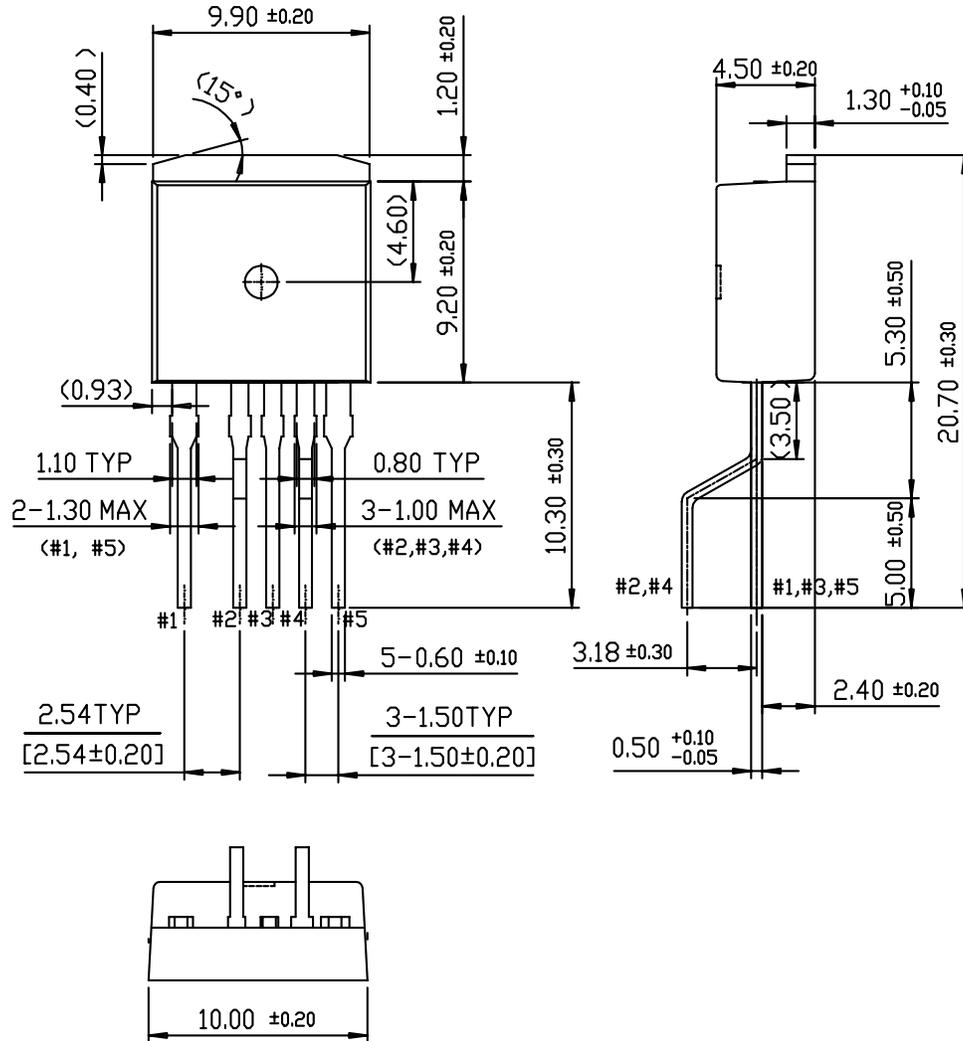
Package Dimensions (Continued)

### I2-PAK-5L



Package Dimensions (Continued)

I2-PAK-5L(Forming)



## Ordering Information

Product Number	Package	Marking Code	BVDSS	FOSC	RDS(on)
KA5H0165R-TU	TO-220F-4L	5H0165R	650V	100kHz	8Ω
KA5H0165R-YDTU	TO-220F-4L(Forming)				
KA5M0165R-TU	TO-220F-4L	5M0165R	650V	67kHz	8Ω
KA5M0165R-YDTU	TO-220F-4L(Forming)				
KA5L0165R-TU	TO-220F-4L	5L0165R	650V	50kHz	8Ω
KA5L0165R-YDTU	TO-220F-4L(Forming)				
KA5H0165RN	8-DIP	5H0165R	650V	100kHz	8Ω
KA5M0165RN	8-DIP	5M0165R	650V	67kHz	8Ω
KA5L0165RN	8-DIP	5L0165R	650V	50kHz	8Ω
KA5M0165RI-TU	I <sup>2</sup> -PAK-5L	5M0165RI	650V	67kHz	8Ω
KA5M0165RI-YDTU	I <sup>2</sup> -PAK-5L(Forming)				
KA5L0165RI-TU	I <sup>2</sup> -PAK-5L	5L0165RI	650V	50kHz	8Ω
KA5L0165RI-YDTU	I <sup>2</sup> -PAK-5L(Forming)				

Product Number	Package	Marking Code	BVDSS	FOSC	RDS(on)
KA5H0165RV-TU	TO-220F-4L	5H0165RV	650V	100kHz	8Ω
KA5H0165RV-YDTU	TO-220F-4L(Forming)				
KA5M0165RV-TU	TO-220F-4L	5M0165RV	650V	67kHz	8Ω
KA5M0165RV-YDTU	TO-220F-4L(Forming)				
KA5L0165RV-TU	TO-220F-4L	5L0165RV	650V	50kHz	8Ω
KA5L0165RV-YDTU	TO-220F-4L(Forming)				
KA5H0165RVN	8-DIP	5H0165RV	650V	100kHz	8Ω
KA5M0165RVN	8-DIP	5M0165RV	650V	67kHz	8Ω
KA5L0165RVN	8-DIP	5L0165RV	650V	50kHz	8Ω

TU : Non Forming Type

YDTU : Forming Type

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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